IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

plicant(s):

Michael H. Perrott

METHOD AND APPARATUS FOR BIT ERROR RATE DETECTION

Application No.: 09/888,708

Filed:

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Examiner:

Lamarre, Guy J.

Group Art Unit:

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MAIL STOP AF COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the last decision of the Examiner, rejecting the following claims: 1-32.

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In addition, the Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. § 1.16 or 1.17 that may be required (a) by this paper or (b) during the pendency of this application, and to similarly credit any overpayment, to Deposit 50-0631.

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Respectfully submitted,

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